

**GENERAL DESCRIPTION**

The RC4444 is a monolithic dielectrically isolated crosspoint array arranged into a 4x4x2 matrix. The primary applications are for balanced switching of 600 ohm transmission lines. The ring and tip are selected by selective biasing of the P+ and P- gate.

Designed to replace reed-relays in telephone switchboards, it does not require a constant gate drive to keep the SCR in the "on" condition. It is several orders faster, with no bouncing, and has a much longer operating life than its mechanical counterpart.

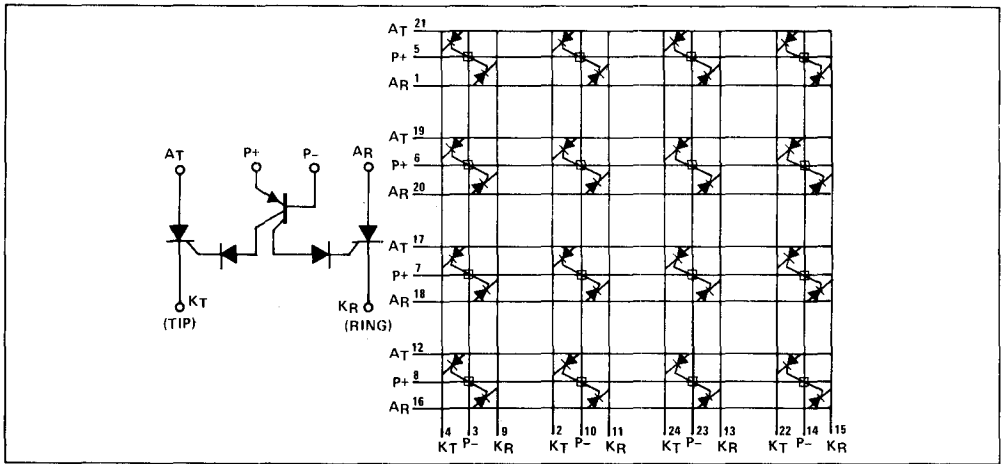
The 16 SCR pairs with the gating system are packaged in a 24 pin dual-in-line package.

The RC4444 is a monolithic pin-for-pin replacement for the MC3416 and MCBH7601.

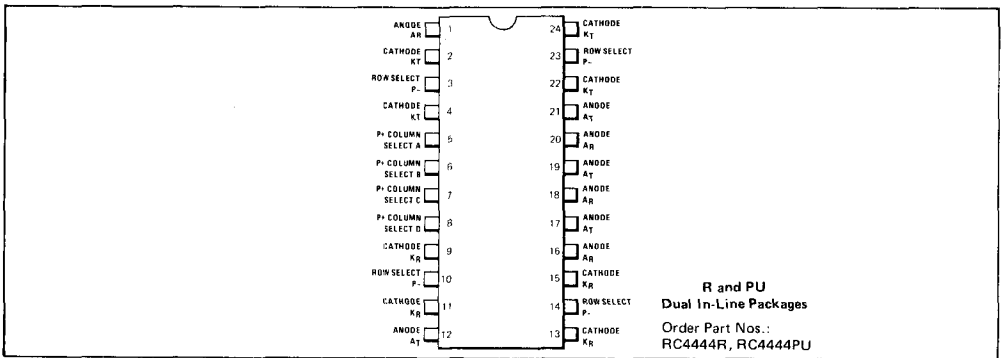
**DESIGN FEATURES**

- Low Bi-Directional  $R_{ON}$
- High  $R_{OFF}$
- Excellent Matching of Gates
- Low Capacitance
- High Rate Firing
- Predictable Holding Current

**SCHEMATIC DIAGRAM**



**CONNECTION INFORMATION**



## ABSOLUTE MAXIMUM RATINGS

Operating Voltage (Note 1) . . . . .	25V	Storage Temperature Range . . . . .	-65°C to +150°C
Internal Power Dissipation (Note 2) . . . . .	900mW	Operating Temperature Range . . . . .	0°C to +70°C
Operating Current per Crosspoint (Note 2) . . . . .	100mA	Lead Temperature (Soldering, 60s) . . . . .	300°C

## ELECTRICAL CHARACTERISTICS (0°C ≤ T<sub>A</sub> ≤ 70°C unless otherwise noted)

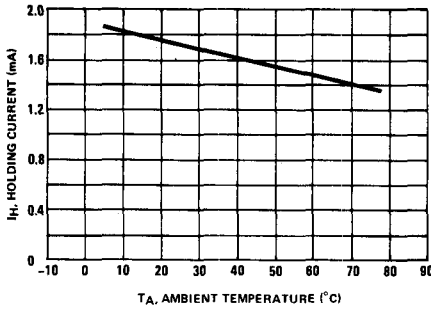
CHARACTERISTIC	SYMBOL	MIN	MAX	UNIT
Anode-Cathode Breakdown Voltage (I <sub>AK</sub> = 25μA)	BVAK	25	-	Vdc
Cathode-Anode Breakdown Voltage (I <sub>KA</sub> = 25μA)	BVKA	25	-	Vdc
Base-Cathode Breakdown Voltage (I <sub>BK</sub> = 25μA)	BVBK	25	-	Vdc
Cathode-Base Breakdown Voltage (I <sub>KB</sub> = 25μA)	BVKB	25	-	Vdc
Base-Emitter Breakdown Voltage (I <sub>BE</sub> = 25μA)	BVBE	25	-	Vdc
Emitter-Cathode Breakdown Voltage (I <sub>EK</sub> = 25μA)	BVEK	25	-	Vdc
OFF State Resistance (V <sub>AK</sub> = 10V)	r <sub>off</sub>	100	-	MΩ
Dynamic ON Resistance (Center Current = 10mA)	r <sub>on</sub>	4.0	12	Ω
(Center Current = 20mA)		2.0	10	
Holding Current (See Figure 10)	I <sub>H</sub>	0.9	3.8	mA
Enable Current (V <sub>BE</sub> = 1.5V)	I <sub>En</sub>	4.0	-	mA
Anode-Cathode ON Voltage (I <sub>AK</sub> = 10mA)	V <sub>AK</sub>	-	1.0	V
(I <sub>AK</sub> = 20mA)		-	1.1	
Gate Sharing Current Ratio @ Cathodes (Under Select Conditions with Anodes Open)	GSh	0.8	1.25	mA/mA
Inhibit Voltage (V <sub>B</sub> = 3.0V)	V <sub>inh</sub>	-	0.3	V
Inhibit Current (V <sub>B</sub> = 3.0V)	I <sub>inh</sub>	-	0.1	mA
OFF State Capacitance (V <sub>AK</sub> = 0V)	C <sub>off</sub>	-	2.0	pF
Turn-ON Time	t <sub>on</sub>	-	1.0	μs
Minimum Voltage Ramp (Which Could Fire the SCR Under Transient Conditions)	dv/dt	800	-	V/μs

### NOTES:

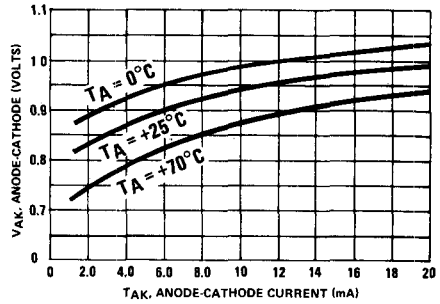
1. Maximum voltage from anode to cathode.
2. Package thermal resistance θ<sub>JA</sub> typically .055°C/mW. Package power dissipation limited to 900mW.

TYPICAL APPLICATIONS

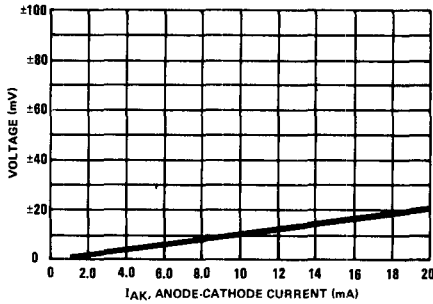
HOLDING CURRENT VERSUS AMBIENT TEMPERATURE



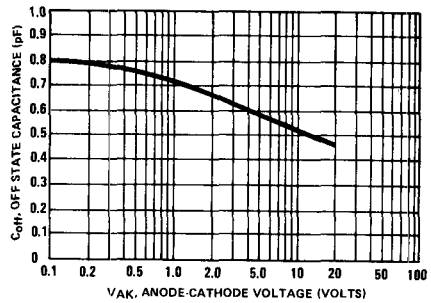
ANODE-CATHODE ON VOLTAGE VERSUS CURRENT AND TEMPERATURE



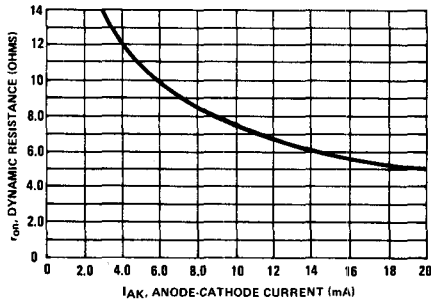
DIFFERENCE IN ANODE-CATHODE ON VOLTAGE (Between Associate Pairs of SCR's) VERSUS ANODE-CATHODE CURRENT



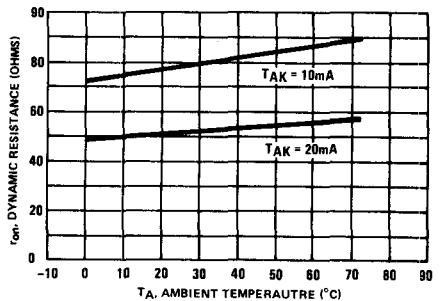
OFF-STATE CAPACITANCE VERSUS ANODE CATHODE VOLTAGE



DYNAMIC ON RESISTANCE VERSUS ANODE-CATHODE CURRENT

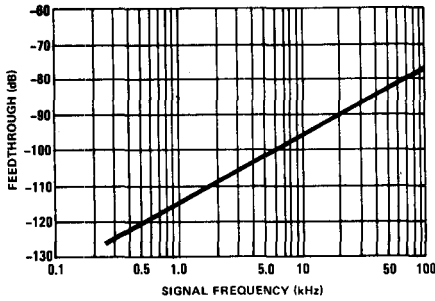


DYNAMIC ON RESISTANCE VERSUS AMBIENT TEMPERATURE

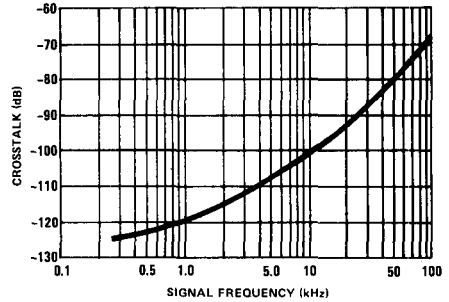


## TYPICAL APPLICATIONS

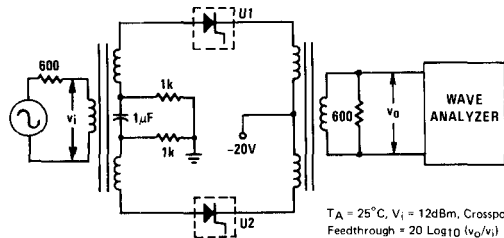
FEEDTHROUGH VERSUS SIGNAL FREQUENCY



CROSSTALK VERSUS SIGNAL FREQUENCY

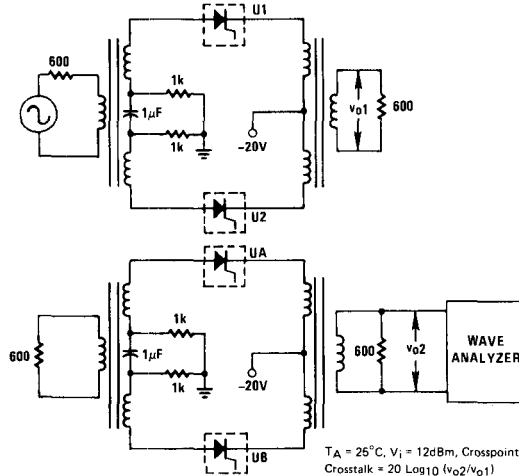


TEST CIRCUIT FOR FEEDTHROUGH VERSUS FREQUENCY



$T_A = 25^\circ\text{C}$ ,  $V_i = 12\text{dBm}$ , Crosspoints Off  
 Feedthrough =  $20 \text{ Log}_{10} (v_o/v_i)$

TEST CIRCUIT FOR CROSSTALK VERSUS FREQUENCY



$T_A = 25^\circ\text{C}$ ,  $V_i = 12\text{dBm}$ , Crosspoints On  
 Crosstalk =  $20 \text{ Log}_{10} (v_{o2}/v_{o1})$